

Jean-Michel Gorius

Student at ENS Rennes

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Research

- Feb. 2021 – **Research Internship**, IRISA, Rennes, FR.
July 2021 *Speculative High-Level Synthesis of Instruction Set Processors.*
Supervisors: Steven Derrien, Simon Rokicki.
- Feb. 2020 – **Research Internship**, Universidad de Murcia, Murcia, ES.
July 2020 *Static Memory Dependence Analysis for Hardware Optimizations.*
Supervisors: Alexandra Jimborean, Alberto Ros.
- 21 Oct. 2019 **Workshop on MLIR for HPC**, Georgia Institute of Technology, Atlanta, GA.
Submitted talk: *Accelerating Climate Modeling: GPU Mapping for Stencil Graphs.*
Jean-Michel Gorius, Tobias Wicky.
- Sept. 2019 – **Research Internship**, ETH Zürich, Zürich, CH.
Dec. 2019 *Efficient GPU Computations of a Parallel Model for Stencils.*
Supervisor: Tobias Grosser.
- May 2019 – **Summer Research Internship**, ETH Zürich, Zürich, CH.
July 2019 *Modeling Stencils in a Multi-Level Intermediate Representation (MLIR).*
Supervisor: Tobias Grosser.
- May 2018 – **Summer Research Internship**, IRISA, Rennes, FR.
Aug. 2018 *Design Space Exploration for Communication Synthesis on Heterogeneous Multi-Core Platforms.*
Supervisor: Steven Derrien.

Teaching

- Sept. 2020 – **Teaching Assistant**, ENS Rennes, Bruz, FR.
Dec. 2020 Master's Compilation class, with Pr. Olivier Ridoux, University of Rennes 1

Studies

- Sept. 2017 – **Magister's degree**, ENS Rennes, Bruz, FR.
Present Research-oriented curriculum.
Class representative.
- Sept. 2018 – **Master's degree**, University of Rennes 1, Rennes, FR.
Present Computer Science Research (SIF) curriculum.
Class representative.
- Sept. 2017 – **Bachelor's degree**, University of Rennes 1, Rennes, FR.
Aug. 2018 Computer Science Research (SIF) curriculum.
Class representative.
- Sept. 2015 – **MPSI/MP Preparatory Classes**, Lycée Jean Moulin, Forbach, FR.
Aug. 2017 Intensive maths, physics and computer science courses.
Valedictorian.
- June 2015 **Baccalaureate**, Lycée Henri Nominé, Sarreguemines, FR.
Scientific major with mechanics, electronics and mathematics specialization.
Valedictorian, Summa cum laude distinction.

Projects

- MLIR **Multi-Level Intermediate Representation**, Contributor.
LLVM **LLVM Compiler Infrastructure**, Contributor.

Languages

French, Native language.

German, Mother language.

English, Spoken, read, written.

Spanish, Beginner.